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Vishay Siliconix

# N-Channel 150 V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω) MAX.	I <sub>D</sub> (A)	Q <sub>g</sub> (TYP.)	
150	0.0094 at V <sub>GS</sub> = 10 V	128	63 nC	
150	0.0110 at V <sub>GS</sub> = 7.5 V	119	63 110	



### **Ordering Information:**

SUP80090E-GE3 (lead (Pb)-free and halogen-free)

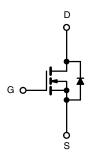
#### **FEATURES**

- ThunderFET® power MOSFET
- Maximum 175 °C junction temperature
- 100 % Rg and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



## **APPLICATIONS**

- · Power supplies:
  - Uninterruptible power supplies
  - AC/DC switch-mode power supplies
  - Lighting
- Synchronous rectification
- DC/DC converter
- Motor drive switch
- DC/AC inverter
- · Solar micro inverter
- Class D audio amplifier



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub> = 25 °C, unless otherwise noted)					
PARAMETER	SYMBOL	LIMIT	UNIT		
Drain-Source Voltage	V <sub>DS</sub>	150			
Gate-Source Voltage	V <sub>GS</sub>	± 20			
Continuous Drain Current /T 150 °C	T <sub>C</sub> = 25 °C		128		
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>C</sub> = 125 °C	I <sub>D</sub>	74	^	
Pulsed Drain Current (t = 100 μs)	I <sub>DM</sub>	240	_ A		
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	60		
Single Avalanche Energy <sup>a</sup>	L=0.1 mn	E <sub>AS</sub>	180	mJ	
Manianum Danian Disabation 8	T <sub>C</sub> = 25 °C		375 b	W	
Maximum Power Dissipation <sup>a</sup>	T <sub>C</sub> = 125 °C	P <sub>D</sub>	125 <sup>b</sup>		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C	

THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	LIMIT	UNIT	
Junction-to-Ambient (PCB Mount) <sup>c</sup>	R <sub>thJA</sub>	40	°C ///	
Junction-to-Case (Drain)	R <sub>thJC</sub>	0.4	°C/W	

#### Notes

- a. Duty cycle  $\leq$  1 %.
- b. See SOA curve for voltage derating.
- c. When mounted on 1" square PCB (FR4 material).

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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{DS}$ $V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		-	-	V	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	2	-	5	V 	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	± 250	nA	
		V <sub>DS</sub> = 150 V, V <sub>GS</sub> = 0 V	-	-	1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ = 150 V, $V_{GS}$ = 0 V, $T_J$ = 125 °C	-	-	100	μA	
		V <sub>DS</sub> = 150 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C	-	-	2	mA	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 10 \text{ V}, V_{GS} = 10 \text{ V}$	90	-	-	Α	
Duain Caluma On State Resistance 2	Б	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A	-	0.0078	0.0094	0	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 7.5 \text{ V}, I_D = 30 \text{ A}$	-	0.0087	0.0110	Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A	-	52	-	S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>		-	3425	-	pF	
Output Capacitance	C <sub>oss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 75 V, f = 1 MHz	-	535	-		
Reverse Transfer Capacitance	C <sub>rss</sub>		-	26	-		
Total Gate Charge <sup>c</sup>	Qg		-	63	95	nC	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = 75 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 60 \text{ A}$	-	19.5	-		
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>		-	20.5	-		
Gate Resistance	$R_g$	f = 1 MHz	1.5	3	5	Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>		-	15	30		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 75 \text{ V}, R_{L} = 1.25 \Omega$	-	114	220		
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong 60 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$	-	28	56	ns	
Fall Time <sup>c</sup>	t <sub>f</sub>		-	8	16		
Drain-Source Body Diode Ratings ar	nd Characteri	stics <sup>b</sup> (T <sub>C</sub> = 25 °C)					
Pulsed Current (t = 100 μs)	I <sub>SM</sub>		-	-	240	Α	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 30 A, V <sub>GS</sub> = 0 V	-	0.73	1.2	V	
Reverse Recovery Time	t <sub>rr</sub>		-	110	220	ns	
Peak Reverse Recovery Charge	I <sub>RM(REC)</sub>	$I_F = 30 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}$	-	10	20	Α	
Reverse Recovery Charge	Q <sub>rr</sub>		-	0.5	1	μC	

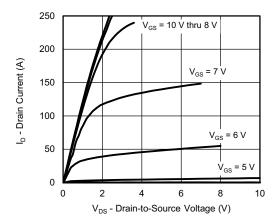
#### **Notes**

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

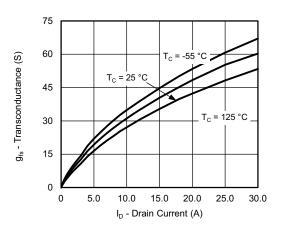
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



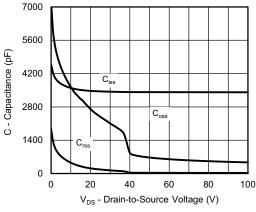
## **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



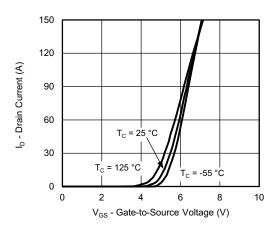
## **Output Characteristics**



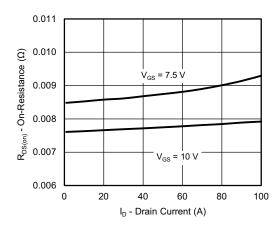
Transconductance



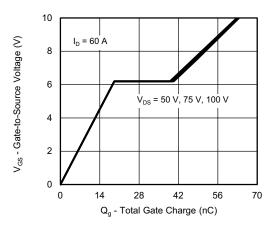
Capacitance



**Transfer Characteristics** 



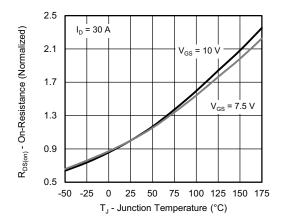
On-Resistance vs. Drain Current



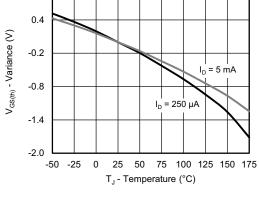
**Gate Charge** 



## **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)

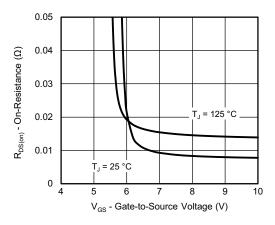


On-Resistance vs. Junction Temperature

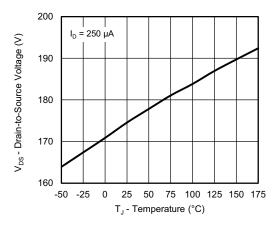


1.0

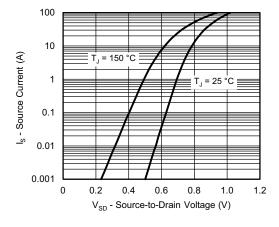
**Threshold Voltage** 



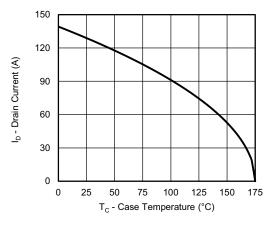
On-Resistance vs. Gate-to-Source Voltage



Drain Source Breakdown vs. Junction Temperature



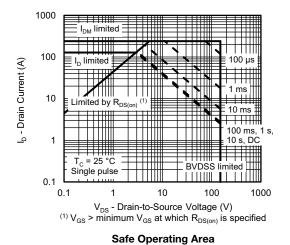
**Source Drain Diode Forward Voltage** 

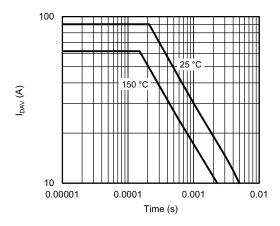


**Current De-Rating** 

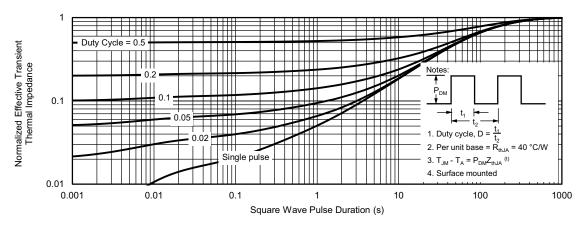


## **THERMAL RATINGS** (T<sub>A</sub> = 25 °C, unless otherwise noted)





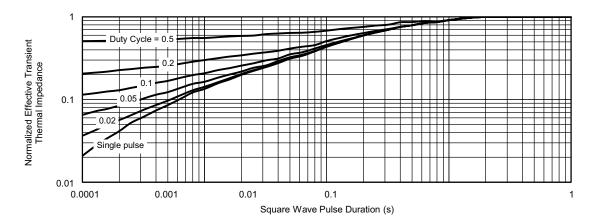
 $I_{\text{DAV}}$  vs. Time



Normalized Thermal Transient Impedance, Junction-to-Ambient



## THERMAL RATINGS (T<sub>A</sub> = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

#### Note

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction to Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction to Case (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="https://www.vishay.com/ppg?65384">www.vishay.com/ppg?65384</a>.



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## **TO-220AB**



	D2

	MILLIMETERS		INCHES	
DIM.	MIN.	MAX.	MIN.	MAX.
А	4.25	4.65	0.167	0.183
b	0.69	1.01	0.027	0.040
b(1)	1.20	1.73	0.047	0.068
С	0.36	0.61	0.014	0.024
D	14.85	15.49	0.585	0.610
D2	12.19	12.70	0.480	0.500
Е	10.04	10.51	0.395	0.414
е	2.41	2.67	0.095	0.105
e(1)	4.88	5.28	0.192	0.208
F	1.14	1.40	0.045	0.055
H(1)	6.09	6.48	0.240	0.255
J(1)	2.41	2.92	0.095	0.115
L	13.35	14.02	0.526	0.552
L(1)	3.32	3.82	0.131	0.150
ØΡ	3.54	3.94	0.139	0.155
Q	2.60	3.00	0.102	0.118
ECN: T14-0413-Rev. P, 16-Jun-14 DWG: 5471				

#### Note

 $<sup>^{\</sup>star}$  M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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